Visiting professors Veljko Milutinovic and Sasa Stojanovic give the seminar "Compilation for FPGA and the maxeler approach"

**Date:** Monday, 14. May 2012

**DATES:**
- May 14th from 14:00 to 19:00 h. room C6-E106
- May 15th from 15:00 to 17:00 h. room C6-E106
- May 16th to 18th from 15:00 to 16:00 h. room C6-E106

Course topics:
1. Nested loops with operations on array elements
2. Conditional processing of array elements
3. Iterative equation solver
4. Multiplication of two matrices stored in local DRAM
5. Pattern matching
6. Iterating through 3D data structures

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